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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/052,082	01/17/2002	William D. Farwell	PD-00W137	5826
23915	7590	09/22/2004	EXAMINER	
PATENT DOCKET ADMINISTRATION RAYTHEON SYSTEMS COMPANY P.O. BOX 902 (E1/E150) BLDG E1 M S E150 EL SEGUNDO, CA 90245-0902			KIM, KENNETH S	
		ART UNIT	PAPER NUMBER	2111
DATE MAILED: 09/22/2004				

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)
	10/052,082	FARWELL ET AL.
	Examiner	Art Unit
	Kenneth S KIM	2111

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

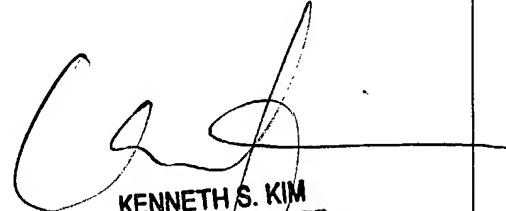
- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) Responsive to communication(s) filed on 17 January 2002.
- 2a) This action is **FINAL**. 2b) This action is non-final.
- 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) Claim(s) 1-16 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) Claim(s) 9-15 is/are allowed.
- 6) Claim(s) 1-8 and 16 is/are rejected.
- 7) Claim(s) _____ is/are objected to.
- 8) Claim(s) _____ are subject to restriction and/or election requirement.



KENNETH S. KIM
PRIMARY EXAMINER

Application Papers

- 9) The specification is objected to by the Examiner.
- 10) The drawing(s) filed on _____ is/are: a) accepted or b) objected to by the Examiner.
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 a) All b) Some * c) None of:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)	4) <input type="checkbox"/> Interview Summary (PTO-413)
2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)	Paper No(s)/Mail Date. _____
3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date _____	5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)
	6) <input type="checkbox"/> Other: _____

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1. Claims 1-16 are presented for examination.
2. The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.
3. Claims 1-8 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

Claim 1, "said switching nodes" lacks antecedent basis.
4. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

- (a) the invention was known or used by others in this country, or patented or described in a printed publication in this or a foreign country, before the invention thereof by the applicant for a patent.
- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- (e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

5. Claims 1-8 and 16 are rejected under 35 U.S.C. 102(e) as being anticipated by Mitchler, U.S. Patent Application Publication No. 2003/0037200.

Mitchler teaches the invention as claimed in claim 1 including a reconfigurable processor (col. 11, line 1) comprising:

- (a) a substrate (par. 11, line 2),
- (b) a plurality of nodes disposed on said substrate (fig. 2; 104, 106, 108, 110),
- (c) at least one plurality of circuits disposed on said substrate (112), each of said circuits being operatively connected, and thereby clustered (par. 18, line 12), with respect to a first of said switching nodes, and

further teaches as in claims 2-8,

- (d) addressing means for selectively addressing said circuits (par. 20, line 5; par. 21, line 9) – claim 2,
- (e) wherein said node is a crossbar switch (par. 18, line 7) connecting arithmetic logic units (par. 13, 2nd line from bottom) – claims 3-6,
- (f) wherein at least one of said node is memory node including plurality of memories (par. 13, 2nd line from bottom) – claims 7 and 8.

The method claim 16 with selectively activating and communicating data to circuits in the node (par. 15, line 8; par. 20, line 9) is equivalently rejected based on the same reason.

6. Claims 1-8 and 16 are rejected under 35 U.S.C. 102(e) as being anticipated by McWilliams et al, U.S. Patent Application Publication No. 2003/0040898.

McWilliams et al teaches the invention as claimed in claim 1 including a reconfigurable processor (col. 11, line 1) comprising:

- (a) a substrate (par. 34, line 2),

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- (b) a plurality of nodes disposed on said substrate (fig. 4; 402, 404, 406, 408),
- (c) at least one plurality of circuits disposed on said substrate (fig. 5, 504-518), each of said circuits being operatively connected, and thereby clustered (par. 36, line 2), with respect to a first of said switching nodes (par. 36, line 6), and further teaches as in claims 2-8,
- (d) addressing means for selectively addressing said circuits (par. 35, line 28) – claim 2,
- (e) wherein said node is a crossbar switch (par. 36, line 6) connecting arithmetic logic units (par. 36, line 8) – claims 3-6,
- (f) wherein at least one of said node is memory node including plurality of memories (can be any type of device) – claims 7 and 8.

The method claim 16 with selectively activating and communicating data to circuits in the node (par. 35, line 30) is equivalently rejected based on the same reason.

7. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Duxbury taught a multi-node data processing system.

Farrell et al taught a multi-node computer system.

New et al taught a configurable matrix of multipliers.

Pincus et al taught a method of accessing memory in a matrix processor system.

Priest et al taught an interconnected processor memory clusters.

8. Claims 9-15 are allowed over the prior art of record.

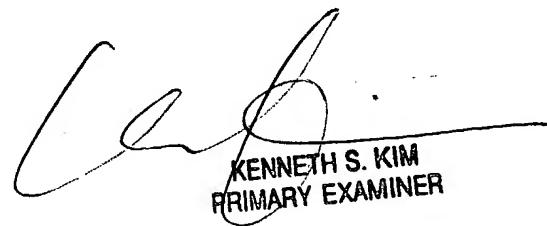
The references of record do not teach selectively addressing clusters of crossbar switch memory clusters interconnecting crossbar switch arithmetic clusters on a substrate.

9. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Kenneth S KIM whose telephone number is (703) 305-9693. The examiner can normally be reached on M-F (8:30-17:00).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mark Rinehart can be reached on (703) 305-4815. The fax phone numbers for the organization where this application or proceeding is assigned are (703) 872-9306 for regular communications and (703) 872-9306 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 305-3900.

September 17, 2004



KENNETH S. KIM
PRIMARY EXAMINER